



ShetkariShikshanPrasarak Mandal's

## Jaywant College of Engineering and Polytechnic, K.M. Gad

(Approved by AICTE, New Delhi, DTE, Govt. of Maharashtra, Affiliated to BATU, Lonere & MSBTE Mumbai)

### Department of Electronics and Tele-Communication Engineering

## JaywantTechFest 2K26 – ‘A Moment of Full Technology’

# Event Report

## Circuit Clash – Circuit Simulation

*“Design • Build • Test the Circuit”*

**Date: 10/03/2026**

### Event Details

<b>Event Name</b>	Circuit Clash(circuit simulation)
<b>Occasion</b>	Jaywant TechFest 2K26
<b>Organized by</b>	Department of Electronics & Tele-Communication Engineering
<b>College</b>	Jaywant College of Engineering & Polytechnic, K.M. Gad
<b>Date</b>	Saturday, 7 March 2026
<b>Venue</b>	JCEP Campus – E&TC Laboratory
<b>Participation</b>	Individual / Team of maximum 2 members
<b>Entry Fee</b>	Rs. 100/- per entry

### About the Event

Circuit Clash is a electronics circuit design and troubleshooting competition organized by the Department of Electronics & Tele-Communication Engineering as part of JaywantTechFest 2K26. The event aimed to foster programming skills, logical thinking, and problem-solving abilities among students. Themed around “Design • Build • Test the Circuit,” the competition provided a platform for students to showcase their electronics circuit design and troubleshooting expertise.

The competition was held in the college computer laboratory and witnessed enthusiastic participation from students across different departments. Inter-branch participation was allowed, making the event more diverse and competitive. Students competed in a two-round format designed to test both theoretical aptitude and practical coding skills.

## Prize Details

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Position	Prize Amount	Eligibility
1st Prize	Rs. 1500/-	Winner
2nd Prize	Rs. 1000/-	Runner-Up
3rd Prize	Rs. 500/-	2nd Runner-Up

## Competition Format

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The event was conducted in one round to ensure a fair and thorough evaluation of participants' electronics circuit design and troubleshooting skills:

### Simulation Round

The Simulation Round was conducted to test the participants' knowledge of electronic circuit design and analysis. In this round, participants were required to design and simulate a given electronic circuit using simulation software in the laboratory. Each team was provided with a computer system to complete the task within the given time. The duration of this round was 1 hour. Participants had to verify the circuit output and explain the working of the designed circuit. The judges evaluated the participants based on circuit accuracy, simulation results, and understanding of the circuit operation.

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## General Rules & Guideline

Participation: Individual / Team of maximum 2 members.

1. Inter-branch participation allowed.
2. Participants must carry their college ID card.
3. Participants must report 15 minutes before the event.
4. Time of Event: 10:30am -11:30 am(Round-1), 12:30 pm-1:30 pm (Round-2)
5. No mobile phones or study materials allowed.
6. Any malpractice leads to immediate disqualification.
7. Judge's decision will be final and binding.
8. Venue: Microcontroller Programming Lab(Third Floor)
9. In case of tie:
  - Completion time will be considered.
  - Additional surprise problem may be given.

## Round 1: Circuit Design Challenge

- Participants will be given a problem statement.
- They must:
  - Design the circuit in Proteus.
  - Simulate and verify output.
- Time Limit: 1 hour.
- Evaluation based on:
  - Correctness (40%)

- Circuit Optimization, Explanation (20%)
- Simulation Accuracy (20%)
- Time Management (20%)

## Round 2: Circuit Design Round (Final Round)

Participants will be given another problem statement

- Time Limit: 30 minutes.
- Fastest correct solution wins.


## Event Coordinators

Student Coordinator	Faculty Coordinator
Samrth Dharme – 8999115434 Samrudhi Garud– 7972405116	Prof. S. P. Dawane – 8855883397 Prof. P. R. Shewale – 9096126033

## Judge

Judge Name	Designation
Dr. M.A.Natu	Assistant Professor

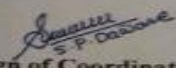
## Winners

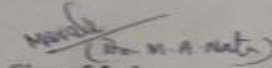

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 (Approved by AICTE, New Delhi, DTE, Govt. of Maharashtra, Affiliated to MSBTE, Mumbai & DBATU,  
 Lonere)  
**JAYWANT TECHFEST 2K26**

Date: 07/03/2026

Name of Department: *Electronics & Telecommunication*  
 Name of Event: *Circuit clash*  
 Name of Coordinators: *Prof. S. P. Dawane & Prof. P. R. Shewale*  
 Name of Judges: *Dr. M. A. Natu*

Sr.No	Rank	Name of Student	College Name	Class	Branch
1	1 <sup>st</sup>	Yash sachin Mane	RIT	TY	} ELTC
2	2 <sup>nd</sup>	Amituddha Patil	RIT	TY	
3	3 <sup>rd</sup>	Swarnil shinde	RIT	SY	

  
 Sign of Coordinator

  
 Sign of Judges

## Conclusion

The Circuit Clash event was successfully organized and concluded under the guidance of the Department of Electronics & Tele-Communication Engineering. The event provided students with a valuable opportunity to demonstrate their electronics circuit design and troubleshooting skills in a competitive environment. Participants showed great enthusiasm, logical thinking, and sportsmanship throughout the competition. The event was a significant highlight of Jaywant Tech-Fest 2K26 and contributed meaningfully to the technical development of students.

## Circuit Clash Event Photos:



**Faculty Co-ordinator**

Asst.Prof.S.P.Dawane & P.R.shewale

**Head of Department**

Mr Yadav P.A

**Principal**

Dr. A. J. Gujar